DPS Level 4 Requirements Corrections for NCR Nos. ECSed03495 & ECSed03551 CCR 96-1149A From Baseline 0916

Table 1 - Reference

L4 id	req_key	rel	req_type	req_status	ver_method	ver_status	CCR	clarification	text	RBR_id	req_key	req_category	segment	req_type	s_verif_method	s_verif_stat	a_verif_method	a_verif_stat	CCR	text	interpretation text	clarification
S-DPS-21230	4432	A	functional	approved	demo	unverified		Wallclock monitoring for runaway PGEs is adequate while dispatching of one PGE per CPU is in affect.	The PRONG CI shall take a predetermined error recovery action if the maximum ~~CPU~~ wallclock time requirements defined for that PGE execution has been exceeded by an adaptable percentage value.	PGS-0340#A	7398	mission essential	SDPS	functional	test	unverified	test	unverified	96-0968A	The PGS shall utilize fault isolation tools provided by the LSM for the PGS and its subsystems.	"PGS and its subsystems" = PDPS, LSM = MSS (MSS provides the tools used for fault detections).	
										PGS-0340#B	7402	mission essential	SDPS	functional	test	unverified	test	unverified	96-0968A	The PGS shall utilize fault isolation tools provided by the LSM for the PGS and its subsystems.	"PGS and its subsystems" = PDPS, LSM = MSS (MSS provides the tools used for fault detections).	

~~S-DPS-21240~~	~~4433~~	~~A~~	~~functional~~	~~approved~~	~~demo~~				~~The PRONG CI shall take a predetermined error recovery action if the maximum memory usage requirements defined for that PGE has been exceeded by an adaptable percentage value.~~	~~PGS-0340#A~~	7398	mission essential	SDPS	functional	test	un-verified	test	un-verified	96-0968A	The PGS shall utilize fault isolation tools provided by the LSM for the PGS and its subsystems.	"PGS and its subsystems" = PDPS, LSM = MSS (MSS provides the tools used for fault detections).	
~~S-DPS-21240~~										~~PGS-0340#B~~	7402	mission essential	SDPS	functional	test	un-verified	test	un-verified	96-0968A	The PGS shall utilize fault isolation tools provided by the LSM for the PGS and its subsystems.	"PGS and its subsystems" = PDPS, LSM = MSS (MSS provides the tools used for fault detections).	

Table 2 - Level 4 Deletions and Modifications

L4 id	req_key	rel	req_type	req_status	ver_method	ver_status	CCR	clarification	text
S-DPS-21230	4432	A	functional	approved	demo	unverified		Wallclock monitoring for runaway PGEs is adequate while dispatching of one PGE per CPU is in affect.	The PRONG CI shall take a predetermined error recovery action if the maximum ~~CPU~~ wallclock time requirements defined for that PGE execution has been exceeded by an adaptable percentage value.
~~S-DPS-21240~~	~~4433~~	~~A~~	~~functional~~	~~approved~~	~~demo~~				~~The PRONG CI shall take a predetermined error recovery action if the maximum memory usage requirements defined for that PGE has been exceeded by an adaptable percentage value.~~